

501.37370CC4

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: N. OHASHI, et al.
Application No.: 10/760,292
Filed: January 21, 2004
For: PROCESS FOR MANUFACTURING SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE
Group AU: 2812
Examiner: Ha T. Nguyen
Confirm. No.: 9846

ADDITIONAL CLAIMS FEE TRANSMITTAL

Mail Stop: Amendment - Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

July 21, 2005

Sir:

Transmitted herewith is an **Amendment** in the above-identified application.☐ No additional fee is required.☒ Also attached: **Credit Card Payment Form, Terminal Disclaimer.**

The fee (if applicable) has been calculated as shown below:

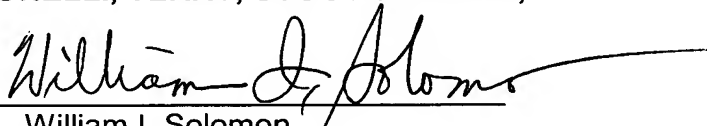
	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	22	20	2	x \$50 =	\$100.00
Independent Claims	2	3	0	x \$200 =	\$0.00
TOTAL FEE DUE					<u>\$100.00</u>

[X] A Credit Card Payment form in the amount of **\$100.00** is attached hereto.

[X] The Commissioner is authorized to charge any deficiencies in fees associated with this communication or credit any overpayment, to Deposit Account No. 01-2135, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17 (case 501.37370CC4).

Respectfully Submitted,

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